



## RESUME

### Mr.Anoop Kumar Khambra

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### Address:

H.No 89 Bijli Colony Near Omega Factory  
Infrount of Gurudwara.

Anand Nagar ,Bhopal (M.P.) 462022

## CAREEROBJECTIVE

To work in dynamic and challenging environment, to keep value of myself and simultaneously contribute to growth and success of the organization, I represent.

## EXPERIENCE

- Currently working as a Controller of Examination in **ARYAVART UNIVERSITY , SEHOR (M.P.)** from date 01- July -2024 till date .
- 4 Year working as a Deputy Exam Controller & Associate Professor in Electronics & Communication Engineering in **MADHYANCHAL PROFESSIONAL UNIVERSITY, BHOPAL** from date 14 July 2020 to 30-06-2024.
- 7 Year teaching Experience as an Associate Professor, I/C HOD in Electronics & Communication Engineering, in **PATEL COLLEGE OF SCIENCE AND TECHNOLOGY** Bhopal from date 11 March 2013 to July 2020.
- 06 Month school teaching Experience in **Sagar Public School** Nasrullaganj Dist. Raisen MP from date 01July 2012 to 22 September 2012
- 1Year Experience as a **PBS IN HDFC BANK** Noida Sector 26 Branch & Noida Sector 01 Branch from 29 September 2011 to 30 May 2012.
- 06 Month experience as a Drive Test Engg. In **Metro Wireless Engineering (India) Pvt. Ltd.** From date 23 Feb 2012 to 06 August 2012.
- 4 Years Teaching Experience as an Assistant Professor in Electronics & Communication Engineering Department in **RAJASTHAN INSTITUTE OF ENGINEERING AND TECHNOLOGY, JAIPUR** from date 06 August 2008 to 30 November 2011.

## ACADEMIC QUALIFICATION

Course	University/Board	Pass on year	Result
<b>Ph.D.</b> in Electronics & Communication Engineering	Madhyanchal Professional University, Bhopal (M.P.)	2025*	Final Viva *
<b>M.Tech</b> in Micro Electronics & VLSI Design	NIIST (RGPV Technology Bhopal) (M.P.)	2018	Pass 79.10% (First Division With Honours)
<b>M.Tech.</b> in Digital Communication	RIET Jaipur, RTU (Rajasthan Technical University Kota)	2013	Pass 61.08% (First Division)
<b>B.E.</b> in Electronics & Telecommunication Engineering	A.M.I.E.T.E New Delhi	2008	Pass 65.08% (First Division)
<b>Class 12<sup>th</sup> PCM</b>	M.P Board	2001	Pass 59.3% (Second Division)
<b>Class 10<sup>th</sup></b>	M.P Board	1999	Pass 49.3% (Second Division)

### SUMMARY / PROFILE STATEMENT:

- A concise overview of the candidate's expertise and experience in examination management.
- Highlight their ability to manage the entire exam process, from planning to result processing.
- Emphasize their commitment to maintaining exam integrity and ensuring fair and reliable results.

### KEY RESPONSIBILITY:

- **Exam Planning and Scheduling:**
  - Developing and announcing examination calendars.
  - Ensuring timely and efficient scheduling of exams.
- **Paper Preparation:**
  - Overseeing the preparation and printing of question papers.
  - Maintaining confidentiality and integrity of exam papers.
- **Exam Conduct:**
  - Ensuring smooth and secure conduct of examinations.
  - Implementing measures to prevent malpractices.

- **Result Processing:**

- Overseeing the evaluation and processing of answer sheets.
- Ensuring the accuracy and reliability of results.

- **Data Management:**

- Maintaining student records and exam-related data.
- Analyzing examination results and identifying areas for improvement.

- **Quality Assurance:**

- Implementing and leading continuous quality improvement initiatives.
- Monitoring and addressing complaints related to examinations.

- **Compliance:**

- Ensuring compliance with relevant regulations and university guidelines.
- Understanding and applying relevant statutes and ordinances.

## INTERESTING SUBJECTS

DSP, SS, CONTROL SYSTEM ,NA, DLD, DE, EMT, EDC, AWP, VLSI DESIGN, MMC etc

## TECHNICAL PROFICIENCY

MSOffice, MATLAB,CST, ie3d, Logisim,TinaPro,C.

## EVENTS ORGANIZED

<b>Event-1:</b>	<b>Two days Virtual International Conference on “ Emerging Trends in Engineering, Science, Management &amp; Commerce”</b>
<b>Role:</b>	Co-Convener-ETESMC-2021
<b>Date:</b>	21 <sup>st</sup> & 22 <sup>nd</sup> May-2021
<b>Organization:</b>	Madhyanchal Professional University, Bhopal, in association with:-Advance Research Educational Society (ARES)
<b>Event-2:</b>	<b>Three days Online National Work shop on “Research methodology &amp; Scientific Tools”</b>
<b>Role:</b>	Co-Convener-RMST-2021
<b>Date:</b>	14 <sup>th</sup> to 16 <sup>th</sup> June-2021
<b>Organization:</b>	Madhyanchal Professional University, Bhopal.

<b>Event-3:</b>	<b>Two days National Conference on “Advances in Science , Engineering &amp; Technology for Sustainable Development Goals”(NCASETSDG-2022)”</b>
<b>Role:</b>	Organizing Committee Member-NCASETSDG-2022
<b>Date:</b>	06 <sup>th</sup> & 07 <sup>th</sup> December-2022
<b>Organization:</b>	Madhyanchal Professional University, Bhopal

## ACADEMIC PROJETS

**Ph.D.- THESIS TITLE:-** “Dynamic Resource Allocation And Energy Minimization In The NOMA System for 5G Networks Using Deep Learning Algorithms”

**M.TECH. (MICRO ELECTRONICS & VLSI DESIGN)**

**THESIS:-** “LOW POWER FLIP-FLOP GROUPING IN DATA-DRIVEN CLOCK GATING”

**M.TECH. (DIGITAL COMMUNICATION)-**

**THESIS:-** “SINGLE LAYER MONOPOL FRACTAL GEOMETRIC ANTENNA FOR APPLICATION OF ISM/WLAN / BLUETOOTH”

**B.E. MAJOR PROJECT:-** Title:- “FIRE DETECTOR + SEND THIS MESSAGE TO OUR CELL PHONE”

**B.E.- MINORPROJECT:-Title: -THREE PHASE FAULT DETECTOR.**

## TRAININGS

**Title:** - Repair & maintenance of different section in CPC Door Darshan New Delhi .

**Time duration:** - One Month.

**Organization:** - Door Darshan New Delhi

## LIST OF PUBLICATIONS IN JOURNALS

[1] Anoop Kumar Khambra , Dr. Rajesh Kumar Rai, “ENERGY - EFFICIENT RESOURCE ALLOCATION IN THE NOMA NETWORKS USING DEEP LEARNING ALGORITHMS”,

International Journal of Applied Engineering & Technology Vol. 6 No.1, January, 2024 ISSN No: 2633-4828 Page No; 537-556.

[2] Anoop Kumar Khambra, Dr. Rajesh Kumar Rai, “ENHANCING UPLINK COMMUNICATION WITH MULTI- USER DETECTION IN NOMA THROUGH DEEP

NEURAL NETWORKS” International Journal of Innovative Research in Technology and Science ISSN: 2321-1156 www.ijirts.org Volume 12 Issue 2, March 2024 Page No; 464-469.

[3] Anoop Kumar Khambra , B.B.Soni , Dr. Puran Gour, “ LOW POWER FLIP-FLOP GROUPING IN DATA DRIVEN CLOCK GATING” International Journal of Master of Engineering Research and Technology, 2394-6172-(O) Volume-5, Issue-7, July 2018. Page No. 153-158.

[4] Anoop Kumar Khambra , B.B. Soni , “CUSTOMIZED ACCELERATORS FOR LOOP

## WORK DONE

- Monitoring of Faculty Teaching Load.
- Monitoring of Class Time Table.
- Monitoring of Course File & Lecture plan.
- Monitoring of Classes, Labs Conduction.
- Compilation of daily class attendance.
- Monitoring of Scheme & Syllabus.
- Monitoring of Mid Semester, End Semester Theory exam conduction.
- Monitoring of Practical Exam conduction.
- Designing of Academic Calendar.
- Declaration of Semester Registration & Commencement of Class Schedule.
- Compilation of Monthly Course Coverage.
- Compilation of Activities Scheduled & Conducted (Seminar/Webinar/Workshop/FDP/Conference/ExpertLecture/IndustrialVis it)
- Conduct University end semester & yearly examination on time.
- Circulate all the notice as per the requirement.
- Prepared all courses result and declared as NAAC guidelines.
- All work performed required in exam confidential cell, Answer copy valuation cell as well as conduction cell.

## PERSONALDETAILS

Name : - Anoop Kumar Khambra  
Father's Name : - Late Shri. Ashok Kumar  
Khambra Mother's Name : - Mrs.Anuradha Khambra  
Marital Status :- Married  
Nationality :- Indian  
Date of Birth :- 08 December 1984  
Permanent Address: - H.No. 89 Bijli Colony Rasian Road , Near Gurudwara Bhopal(M.P.)  
Languages Known: - Hindi and English  
Hobbies :- Traveling ,Listening-Music.

**Declaration:-** I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.